

# BHAVANI SHANKAR. SURABHI

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Research & Development (R&D)  
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## EDUCATION

M.Sc. Electronics

April 2006

Andhra University

## EXPERIENCE

R&D Embedded Trainee | Efftronics Systems Pvt. Ltd.

Aug 2006 – June 2010

R&D Project Lead | Efftronics Systems Pvt. Ltd.

July 2010 – Nov 2011

R&D Project Manager | Efftronics Systems Pvt. Ltd.

Dec 2011 – Dec 2012

R&D Executive Manager | Efftronics Systems Pvt. Ltd.

Dec 2012 – Present

## SKILLS

- Expertise in developing Internet of Things (IoT) based Smart Solutions.
- Expertise in developing systems with a wide range of embedded processors including Motorola 68000, Intel 8051 core microcontrollers, ARM7 & Cortex-M3 core microcontrollers etc.
- Expertise in designing reliable LED based visual systems using multiplexing and non-multiplexing methods in view of thermal, optical, electrical characteristics.
- Expertise in designing with many interfaces with different types of memories like NAND, NOR Flash, SRAM, SDRAM etc., peripheral ICs, removable memory cards, Real Time Clock ICs, ADC, DAC etc.
- Expertise in designing user interfaces with numeric and alphanumeric keypads, PC keyboards, LED and LCD modules (character and graphic)
- Expertise in designing distributed systems, in different configurations over

network in real time applications

- Expertise in different communication standards and protocols like Serial communication - RS232, RS485, Current Loop, Ethernet, USB, WiFi, Bluetooth, ZigBee, GSM/GPRS etc.
- Expertise in hardware design using small CPLDs to large FPGAs.
- VHDL/Verilog language designing
- Experience in using KEIL MDK-ARM, Lattice Semi ispLEVER Classic, ACTEL LIBERO etc. tools.
- Practical exposure to railway signaling & telecom domain
- Product design as per DESIGN THINKING approach

#### STANDARDS

- Exposure to ISO 9001:2008 quality standards
- Exposure to ECE.R10 EMI/EMC standards
- Exposure to IEC/CISPR standards related to EMI/EMC