Brief profile: KCS Murti

- Graduated in Electronics from Govt. college of engineering, kakinada.
- Masters from BITS, Pilani (1974)
- Worked in Indian Engineering Services (IES) for 6 years in R&D doordarshan
- Worked for 3 years as Asst professor in military college of telecom eng (MCTE), MHOW.
- Worked for 20 years as scientist at Central Electronics Engineering Research Inst (CEERI) also as adjunct facilty for BITS, Pilani during this period. Focus of research is "adapting Information technology to conventional Indian industry"
- Switched to software Industry and worked in GIS for Intergraph consulting, Hyderabad.
- After retirement working as visiting faculty at BITS ,Hyderabad campus.
- During his 43 years of experience due contribution is given by him to research, industry and academics.
- Published around 50 papers and 3 patents.