

Bio-Data

Dr. B. Lakshmi

Associate Professor

Department of Electronics and Communication Eng.

National Institute of Technology, Warangal.

Current Research interests

Embedded Systems, VLSI Architectures, Digital system Design, IoT

Education:

Ph.D, Ph.D thesis title: High speed VLSI CORDIC architectures

Department of Electronics and Electrical Communication Engineering,

Indian Institute of Technology, Kharagpur, W.B, INDIA

2010

M.Tech Electronic Instrumentation,

National Institute of Technology (formerly R.E.C,Warangal), A.P, INDIA

1992

B.Tech Electronics and Communication Engineering,

Nagarjuna University, A.P, INDIA

1986

Responsibilities:

Associate Dean Student welfare: August 2105 till date

Deputy Chief Warden for Ladies Hotels: **May 2014 to July 2015**

Head of the Department: **Feb. 2011 to Feb. 2013**

Reviewer of Elsevier Journal, IEEE TVLSI

IEEE Senior Member

Invited talk : **National conference on women in Science and engineering, WISE2013**

Invited Talk : **“Low Latency and High Throughput VLSI Architectures of CORDIC Algorithm” on Nov. 27th 2013 in the Department of Computer Science and engineering, USF, Florida, USA**

Session Chair : **National conference on Women in Science and Engineering, WISE2013, Dharwad, India**
Session Chair : **IEEE Region 10 Technical Symposium (TENSYP), Malaysia, 2014,**

Research Projects

Principle Investigator: **“Design and Implementation of DSP and Multimedia Instruction set for ANUPAMA Processor” DRDO (Defense Organization), Hyderabad April 2012**
Status : **Completed**

Principle Investigator: **“Intel Atom based embedded systems Laboratory Development”**
Period: **July 2012 to till date**

Principle Investigator: **“Design and FPGA Prototype Development of AFDX End System” RCI, Hyderabad**
Status: **completed**

Supervisor : **Five Ph.D. students**

Visiting faculty

Country : **USA**
Project : **Technical Quality Improvement Program (TEQIP)**
University : **Stevens Institute of Technology, NJ, USA**
Duration : **Nov. 18th 2013- Dec.17th 2013**

Prior Training programs

Country: **United Kingdom,**

Project : India-UK-REC Digital System Design

University : **University of Huddersfield, UK**

Modules Completed : 1. ECAD -5 credits
2. Test and Design for Testability---5Credits
3. Structured Digital design-5 credits
4. ASIC Design 5 Credits

University : **University of Sheffield, UK**

Duration : November 1994 to July 1995

Around 50 publications in various conferences and Journals