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Brief Profile

Dr. M. Rajasekhara Babu born on 1st January 1975 had his education in Anantapur (D.T.), A.P. taking his B.Tech in Electronics and Communication Engineering (E.C.E) from K.S.R.M. College of Engineering (affiliated to S.V. University, Tirupathi) and M.Tech. in Computer Science and Engineering (C.S.E.) from R.E.C Calicut (presently known as NIT Calicut) . He took his Ph.D. degree from VIT University.

After serving Intell Engineering College for two years (2000-2002), he joined VIT University Vellore in 2002 and served in many positions. He was instrumental in establishing Intel Multi-Core Architecture Research Laboratory at VIT University in Multi-Core architectures and he is fondly introduced with this in most of the forums. He produced more than 50 international/National publications in conferences and journals and authored couple of books in the area of Computer Architecture, Compiler Design and Grid Computing. He edited international conference proceedings ObComAPC - 2004 (ISBN: 8177647148), ObCom2006 (ISBN of Vol 1- 9780230630109 & Vol. II- 9780230630116), and ObCom 2011 (ISBN of Vol 269:978-3-642-29218-7 & Vol 270:978-3-642-29215-6) which were published by reputed publishers Allied Publishers Pvt Ltd, Macmillan Publishers India and Springer International Publisher resly.,.

Dr. RajaBabu's works were close to industrial needs of the day and assisted in the development of energy aware software applications for Multi-Core Architectures. He received a best teacher award for year 2005-2006 and rewarded for the best course content made available in Acado (an e. learning portal) in 2007 by VIT University.

Through his interaction with many professional societies such as Computer Society of India (CSI), IEEE, IAEI, he has extensively conducted training programmes, workshops, seminars & conferences of international standing. He also held positions in CSI-Vellore chapter as treasurer, Vice-Chairman, etc.,

Dr. RajaBabu has served in various prestigious positions as a Division Leader (TCS & LT) & Program Manager , Committee member of Board of Studies (SCSE), Committee member of central library (VIT University) , Committee member of Project Based Learning team (Academic Committee), Placement and Training (PaT) Coordinator for Training Programs (TCS, Infosys, Wipro), Committee member of Accreditation committee (SCSE), A university cultural and sports fest -Reviera2011 &12 finance Controller, Project Coordinator (B.Tech(CSE)), Quality Circle Meeting (QCM) Coordinator, School Time table coordinator, Exam Coordinator, Acado e-learning Co Coordinator , Committee member of University Internal quality Assurance Cell (IQAC) and a few others.

But his zeal for technical education and engineering contributions are continuous. For the last ten years, he has served VIT University in vellore, first as a Lecturer, as then Sr. lecturer, Lecturer Selection grade, Asst. professor Selection grade and associate professor. His contributions to the University and computer Science and Engineering fields are noteworthy.